

	Snapdragon 820 SMARC Module	Snapdragon 820 SMARC Development Kit (Android)	
	Supported Features	Alpha release 15thSep2017	Product release Dec 2017
SOC: Qualcomm APQ8096	64-bit Quad Kryo core	○	○
	Adreno 530 3D Graphics @624MHz	○	○
	H.265 4K60 decode, 4K30 encode	○	○
	Qualcomm Hexagon DSP	○	○
Memory:	3GB LPDDR4	○	○
	32GB eMMC	○	○
	Micro SD Slot	○	○
Communication:	PCIe to Gigabit Ethernet + PHY	△	○
	USB2.0 HUB	△	○
	GPS	○	○
	Wi-Fi ¹ + BT (Check Note1)	○	○
SMARC Edge Connector Interfaces:	Gigabit Ethernet x 1 port	△	○
	PCIe x 1 port	○	○
	HDMI2.0 x 1 port	○	○
	USB3.0 host x 2 ports	△	○
	USB 2.0 host x 4 ports	△	○
	USB2.0 device x 1 port	○	○
	MIPI CSI0 x 2 lanes	△	○
	MIPI CSI1 x 4 lanes	△	○
	MIPI DSI0 x 1 port (4 lanes)	△	○
	MIPI DSI1 x 1 port (4 lanes)	x	x
	SD (4bit) x 1 port	○	○
	RTC	○	○
	UART x 6 ports	△	○
	MI2S x 1 port	△	○
	SPI x 2 ports, I2C x 2 ports	△	○
GPIOs	△	○	
Expansion Connector interfaces:	MIPI CSI x 1 port (4 lanes)	x	x
	I2C x 1 port, GPIOs, SLIM bus	x	x
OS Support:	Android Marshmallow or higher	○	○
Display & Touch for development kit	5.5" MIPI Display with Capacitive Touch	○	○
Power Supply:	SOM: 5V through SOM edge	○	○
	Development Kit: 12V External AC/DC Adaptor	○	○
Temperature support:	0° C to +70° C	△	○
Form Factor:	SOM: 82mm x 50mm, SMARC R2.0 compatible	○	○
	Development Kit: 120mm x 120mm, nano ITX Size	○	○

Note1 : The Wi-Fi module used in the SOM is certified for Japan regulations. Check the approval ID listed below;
Japan regulatory approval ID : 003-140224 / D140159003